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List of Documents
Cited by Applicant

(Use several sheets if necessary)

Applicant: Francesco PAPPALARDO et al.

Filing Date: July 18, 2003

Group: 2183

U.S. PATENT DOCUMENTS

Ex's In1		Document Number	Date	Name	Class	Sub- class	Filing Date, if applicable
873	AA1	5,392,423	February 21, 1995	Yetter			
873	AA2	5,670,899	September 23, 1997	Kohdaka			
873	AA3	6,122,751	September 19, 2000	Janssens et al.			

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Sub- class	Trans'n Yes/No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

873	AA4	Moshnyaga, V.G. et al., "Energy Saving Techniques for Architecture Design of Portable Embedded Devices," ASIC Conference and Exhibit 1997 Proceedings, Tenth Annual IEEE, International Portland, OR, USA 7-10 September 1997, New York, NY IEEE September 7, 1997, XP010243383, pp. 163-167, ISBN: 0-7803-4283-6.
873	AA5	European Search Report for application no. EP 02 42 5469 dated July 1, 2003.

Examiner: *JSJ*

Date Considered: 11/18/2005

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